

Session QQ:

High Power Devices

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High power amplifiers create unique needs for power control and combining. This session includes papers on design aspect of high power PIN switches and power combiners. Thermal problems and optimal reverse bias conditions for PIN diodes will be covered. Also several methods of power combining with extremely low loss, covering multi-octave bandwidth in coaxial, will be reported. Novel E and H plane combiners will be discussed. These systems are capable of handling varying levels of power.

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3:30 p.m.-5:00 p.m., Thursday, May 10, 1990
West Ballroom D